IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	
Filing Date	July 21, 200:
Inventor	Luan C. Trai
Assignee	Micron Technology, Inc
Group Art Unit	281
Examiner	Jennifer J. Kennedy
Attorney Docket No	
Title: Methods of Forming Semiconducto	

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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2. Supplemental Information Disclosure Statement

3. PTO-1449

Dated: March 10: 2004

By:

Jaime M. White

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FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

By:

Jennifer J. Taylor, Ph.D.

Reg. No. 48.711

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	4,627
Filing Date July 21,	2003
Inventor Luan C	Tran
Assignee Micron Technology	
Group Art Unit	2811
priority Examiner Kennedy, Jenni	fer J.
Attorney's Docket No	-2358
Title: Methods of Forming Semiconductor Constructions	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: March 10,2004

Bv:

Jennifer J. Taylor, F

Reg. No. 48,71

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